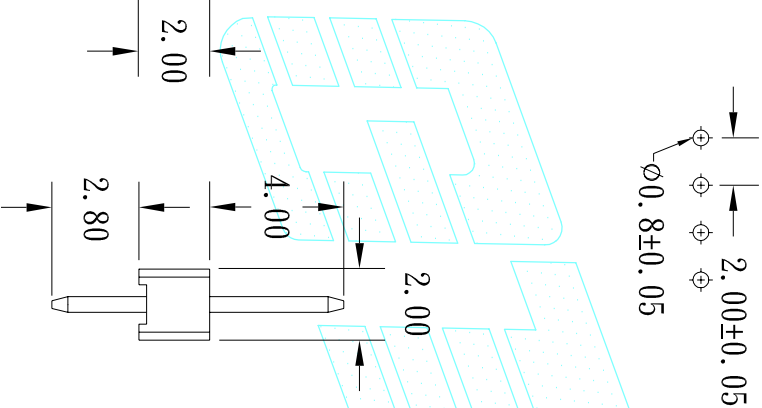
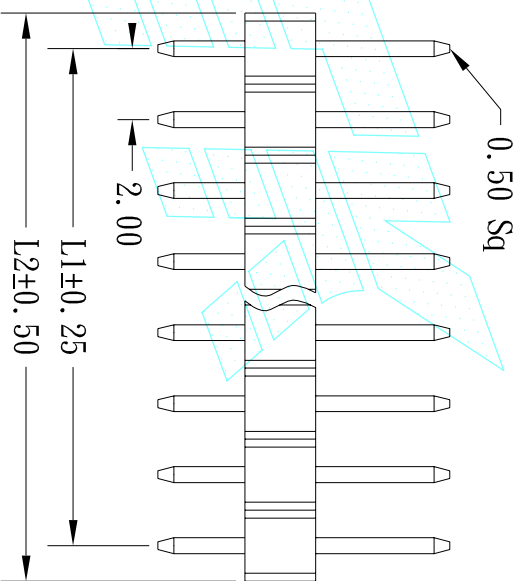
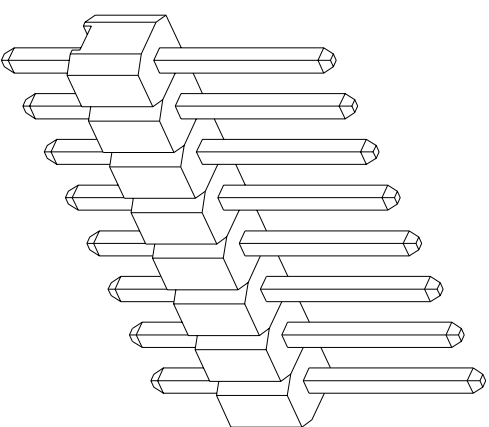


Dimensional & Ordering Information:

Circuits	Dimension mm	
	L1	L2
2	2.00	4.00
3	4.00	6.00
4	6.00	8.00
5	8.00	10.00
6	10.00	12.00
7	12.00	14.00
8	14.00	16.00
9	16.00	18.00
10	18.00	20.00
11	20.00	22.00
12	22.00	24.00
13	24.00	26.00
14	26.00	28.00
15	28.00	30.00
16	30.00	32.00
17	32.00	34.00
18	34.00	36.00
19	36.00	38.00
20	38.00	40.00
21	40.00	42.00
22	42.00	44.00
23	44.00	46.00
24	46.00	48.00
25	48.00	50.00
26	50.00	52.00
27	52.00	54.00
28	54.00	56.00
29	56.00	58.00
30	58.00	60.00
31	60.00	62.00
32	62.00	64.00
33	64.00	66.00
34	66.00	68.00
35	68.00	70.00
36	70.00	72.00
37	72.00	74.00
38	74.00	76.00
39	76.00	78.00
40	78.00	80.00



Recommended
P.C. Board
Hole Layout

- APPLICATION AND FEATURES
1. Side and end stackable (on unbroken edges).
 2. Notches for easy breakable.
 3. Variable pin length, gold or tin plated.
 4. Standoffs facilitate post soldering clean.
 5. Mate with Mini Jumper and P.C. Board connectors e.g.

SPECIFICATIONS

- *Current Rating: 1 Amps
- *Insulator resistance: 5000 Megohms Min.
- *Dielectric withstanding: AC 500 V
- *Voltage: AC 250 V
- *Operating Temperature: -40° - +105°C
- *Contact Material: Brass
- *Insulator Material: Standard:P.B.T
- *Finish: G = All Gold Plated
T = All Tin Plated
H = Tin Plated on Terminal & Gold Plated on Contact

TOLERANCE UNLESS OTHERWISE SPECIFIED		PROJECTION		TITLE	
L INEAR	ANGLE	UNITS		2.00MM	
X. ±	X° ±	MM		PIN HEADER	
.XX ± 0.25	.XX° ± 3°	SCALE		IPART NO	
.XXX ±	.XXX ±	SIZE		A4	
APPD					
CHKD					
DRAW	Tao. Tao.	JAN. 22, 2003			
GOLD-TEK ELECTRIC CO., LTD.		PART NO. PHSS*G(T)(H)20		CAD NO. 1642	
		F:\Draw\PH\PH-0065		SHEET 1 OF 1	
		圖面編號:GTKSD0678-B(1)			